

Implementing NOT & AND logic gates

Spoken Tutorial Project

<https://spoken-tutorial.org>

National Mission on Education through ICT

<http://sakshat.ac.in>

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Learning Objectives

How to implement logic gates



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- NOT



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- AND



System Requirements



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- **LDmicro**



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- **Switchboard module**



Pre-requisites



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- Normal Contact and Coil



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If not, please refer to the relevant tutorials in this series from

<https://spoken-tutorial.org>



Prerequisites - Hardware setup



NOT gate

For a NOT gate the output boolean is inverse of the input



AND gate

AND gate output state is 1 only when all of its input states are 1



Summary

We learnt to implement logic gates

- **NOT**
- **AND**



Assignment

Implement 2-input NAND logic gates

Truth Table:

A	B	NAND(A,B)
0	0	1
0	1	1
1	0	1
1	1	0



About the Spoken Tutorial Project

- Watch the video available at https://spoken-tutorial.org/What_is_a_Spoken_Tutorial
- It summarises the Spoken Tutorial project
- If you do not have good bandwidth, you can download and watch it



Spoken Tutorial Workshops

The Spoken Tutorial Project Team

- Conducts workshops using spoken tutorials
- Gives certificates to those who pass an online test
- For more details, please write to contact@spoken-tutorial.org



Forum questions

- Questions in THIS Spoken Tutorial?
- Visit <https://forums.spoken-tutorial.org/>
- Choose the minute and second where you have the question
- Explain your question briefly
- The Spoken Tutorial project will ensure an answer

You will have to register to ask questions



Forum for specific questions

- Questions not related to the Spoken Tutorial?
- Do you have general / technical questions on the Software?
- Please visit the FOSSEE Forum
<https://forums.fossee.in/>
- Choose the Software and post your question



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THANK YOU!

For more Information, visit our website
<https://fossee.in/>

